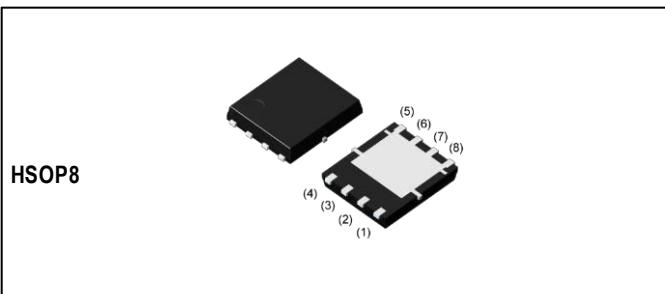
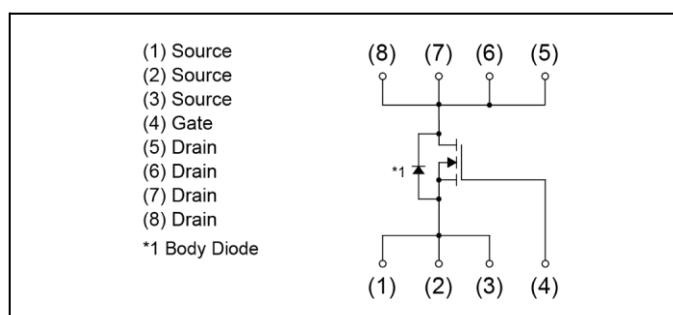


$V_{DSS}$	30V
$R_{DS(on)}(\text{Max.})$	2.1mΩ
$I_D$	±80A
$P_D$	34W

## Outline



## Inner circuit



## Features

- 1) Low on - resistance
- 2) High power package (HSOP8)
- 3) Pb-free lead plating ; RoHS compliant
- 4) Halogen free
- 5) 100% Rg and UIS tested

## Packaging specifications

Type	Packing	Embossed Tape
Reel size (mm)	330	
Tape width (mm)	12	
Basic ordering unit (pcs)	2500	
Taping code	TB1	

## Absolute maximum ratings ( $T_a = 25^\circ\text{C}$ , unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	$V_{DSS}$	30	V
Continuous drain current	$I_{D(\text{max})}$ $\text{at } T_c = 25^\circ\text{C}$	±80	A
	$I_D$ $\text{at } T_a = 25^\circ\text{C}$	±32	A
Pulsed drain current	$I_D(\text{pk})$ $\text{at } T_a = 25^\circ\text{C}$	±128	A
Gate - Source voltage	$V_{GSS}$	±20	V
Avalanche current, single pulse	$I_A(\text{pk})$ $\text{at } T_a = 25^\circ\text{C}$	32	A
Avalanche energy, single pulse	$E_A(\text{pk})$ $\text{at } T_a = 25^\circ\text{C}$	77	mJ
Power dissipation	$P(\text{max})$ $\text{at } T_a = 25^\circ\text{C}$	34	W
	$P(\text{max})$ $\text{at } T_a = 150^\circ\text{C}$	3.0	W
Junction temperature	$T_j$	150	°C
Operating junction and storage temperature range	$T_{stg}$	-55 to +150	°C

**Thermal resistance**

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max	
Thermal resistance, junction - case	$R_{thJ}^{*1}$ C	-	-	3.6	°C/W
Thermal resistance, junction - ambient	$R_{thJ}^{*4}$ A	-	-	41.7	°C/W

**Electrical characteristics ( $T_a = 25^\circ\text{C}$ )**

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 1\text{mA}$	30	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	$I_D = 1\text{mA}$ referenced to $25^\circ\text{C}$	-	28	-	mV/°C
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 24\text{V}, V_{GS} = 0\text{V}$	-	-	1	μA
Gate - Source leakage current	$I_{GSS}$	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	-	-	$\pm 100$	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 1\text{mA}$	1.2	-	2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	$I_D = 1\text{mA}$ referenced to $25^\circ\text{C}$	-	-3.87	-	mV/°C
Static drain - source on - state resistance	$R_{DS(on)}^{*5}$	$V_{GS} = 10\text{V}, I_D = 32\text{A}$ $V_{GS} = 4.5\text{V}, I_D = 32\text{A}$	-	1.4	2.1	mΩ
Gate resistance	$R_G$	f=1MHz, open drain	-	1.4	-	
Forward Transfer Admittance	$ Y_{fs} ^{*5}$	$V_{DS} = 5\text{V}, I_D = 32\text{A}$	35.0	-	-	S

\*1  $T_c = 25^\circ\text{C}$ , Limited only by maximum temperature allowed.

\*2  $P_w \leq 10\mu\text{s}$ , Duty cycle  $\leq 1\%$

\*3  $L = 0.1\text{mH}, V_{DD} = 15\text{V}, R_G = 25\Omega$ , Starting  $T_j = 25^\circ\text{C}$  Fig.3-1,3-2

\*4 Mounted on a Cu board (40x40x0.8mm)

\*5 Pulsed

**Electrical characteristics ( $T_a = 25^\circ\text{C}$ )**

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max	
Input capacitance	$C_{iss}$	$V_{GS} = 0\text{V}$ $V_{DS} = 15\text{V}$ $f = 1\text{MHz}$	-	2850	-	pF
Output capacitance	$C_{oss}$		-	740	-	
Reverse transfer capacitance	$C_{rss}$		-	210	-	
Turn - on delay time	$t_{d(on)}^{*5}$	$V_{DD} = 15\text{V}$ , $V_{GS} = 10\text{V}$ $I_D = 16\text{A}$ $R_L = 0.93\Omega$ $R_G = 10\Omega$	-	21.8	-	ns
Rise time	$t^{*5r}$		-	15.6	-	
Turn - off delay time	$t_{d(off)}^{*5}$		-	74.6	-	
Fall time	$t_f^{*5}$		-	28.5	-	

**IGate charge characteristics ( $T_a = 25^\circ\text{C}$ )**

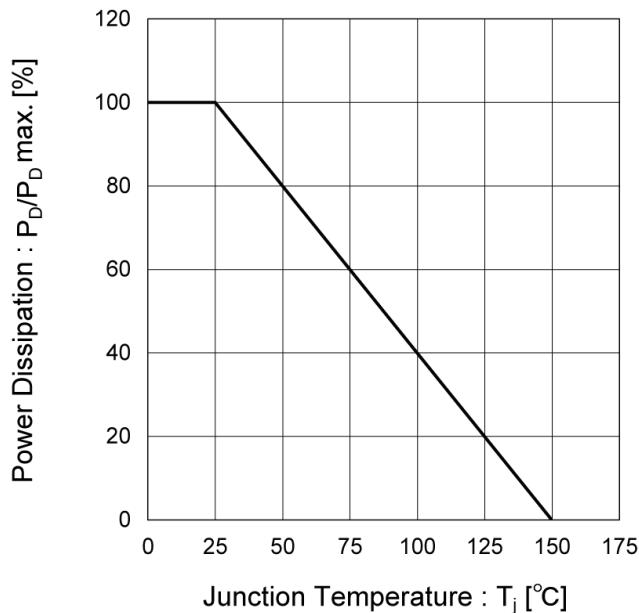
Parameter	Symbol	Conditions	Values			Unit	
			Min.	Typ.	Max		
Total gate charge	$Q^{*5g}$	$V_{DD} = 15\text{V}$ $I_D = 32\text{A}$	$V_{GS} = 10\text{V}$	-	42.8	-	nC
			$V_{GS} = 4.5\text{V}$	-	19.6	-	
Gate - Source charge	$Q_{gs}^{*5}$		-	11.6	-	-	
Gate - Drain charge	$Q_g^{*5d}$		-	6.7	-		

**IBody diode electrical characteristics (Source-Drain) ( $T_a = 25^\circ\text{C}$ )**

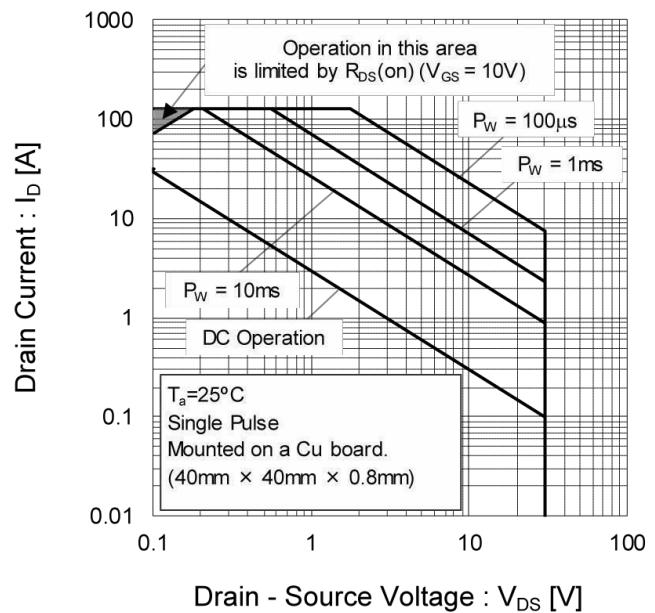
Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max	
Continuous forward current	$I_S$	$T_a = 25^\circ\text{C}$	-	-	2.5	A
Pulse forward current	$I_S^{*2P}$		-	-	128	A
Forward voltage	$V_S^{*5D}$	$V_{GS} = 0\text{V}$ , $I_S = 2.5\text{A}$	-	-	1.2	V
Reverse recovery time	$t_r^{*5r}$	$I_S = 32\text{A}$ , $V_{GS}=0\text{V}$ $di/dt = 100\text{A}/\mu\text{s}$	-	37.8	-	ns
Reverse recovery charge	$Q_{rr}^{*5}$		-	35.2	-	

### Electrical characteristic curves

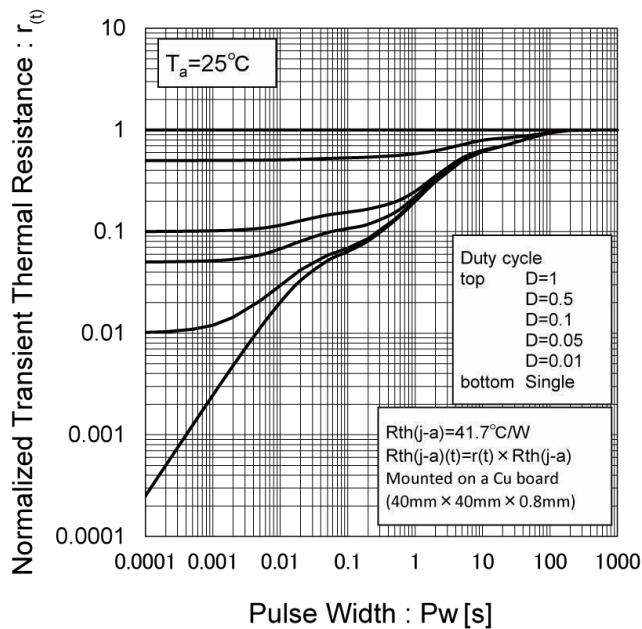
**Fig.1 Power Dissipation Derating Curve**



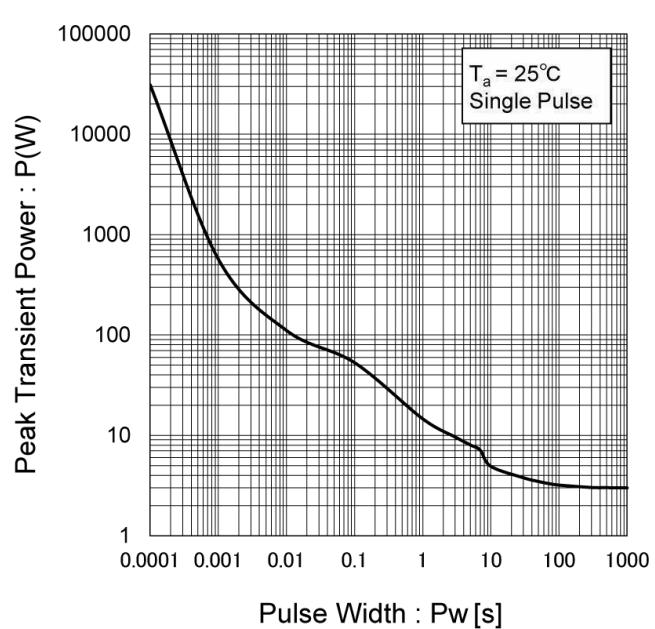
**Fig.2 Maximum Safe Operating Area**



**Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width**

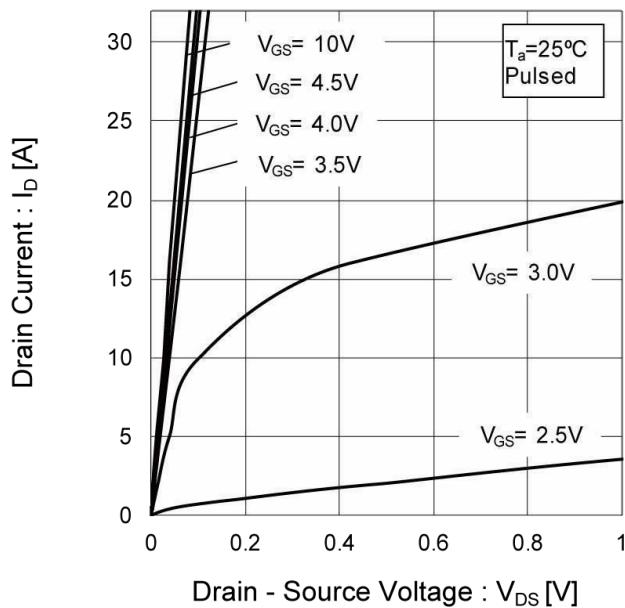


**Fig.4 Single Pulse Maximum Power dissipation**

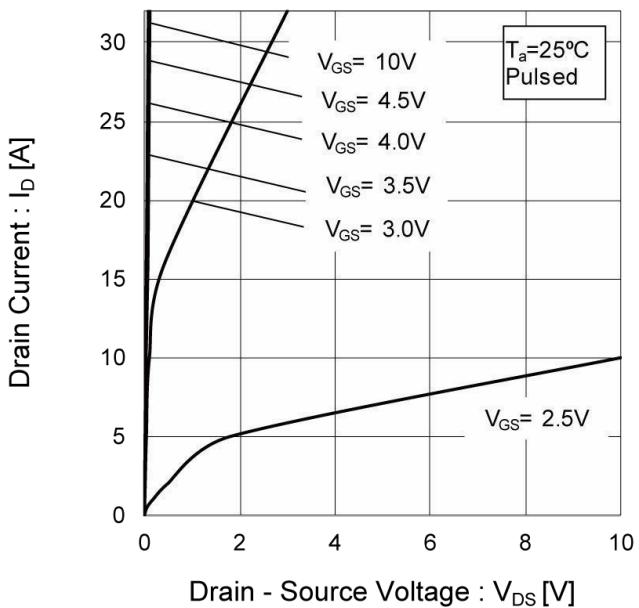


### Electrical characteristic curves

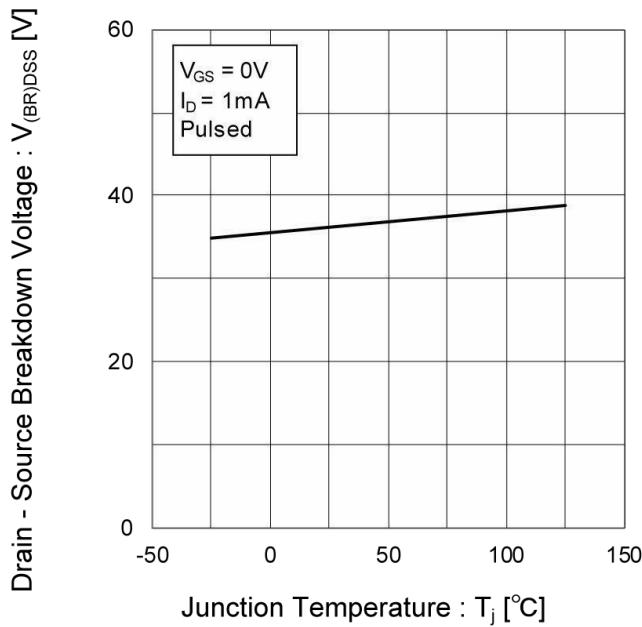
**Fig.5 Typical Output Characteristics(I)**



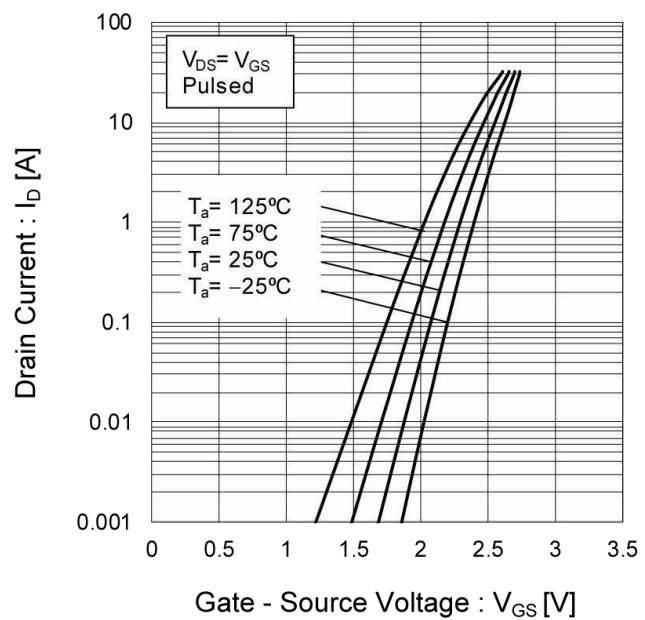
**Fig.6 Typical Output Characteristics(II)**



**Fig.7 Breakdown Voltage vs.  
Junction Temperature**

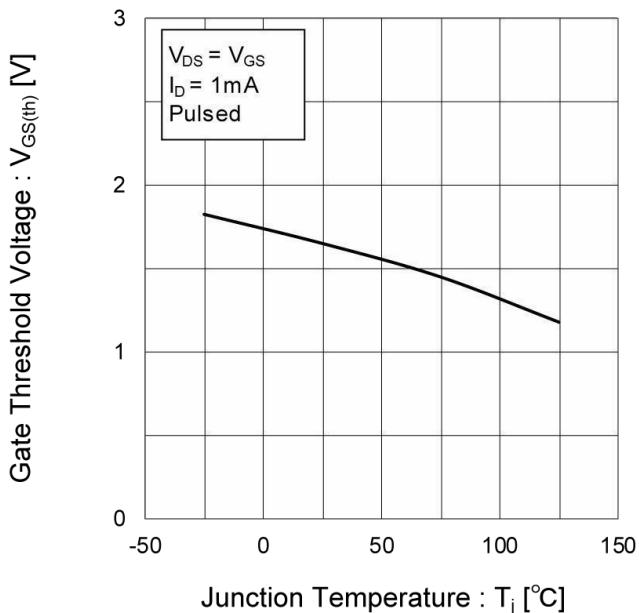


**Fig.8 Typical Transfer Characteristics**

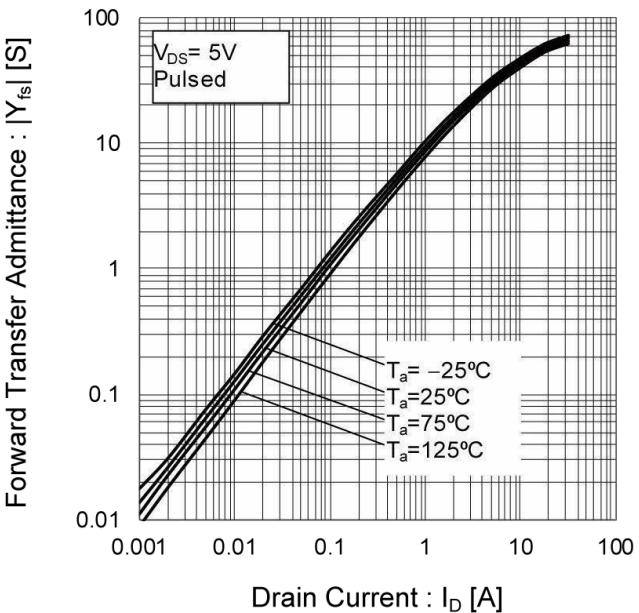


### Electrical characteristic curves

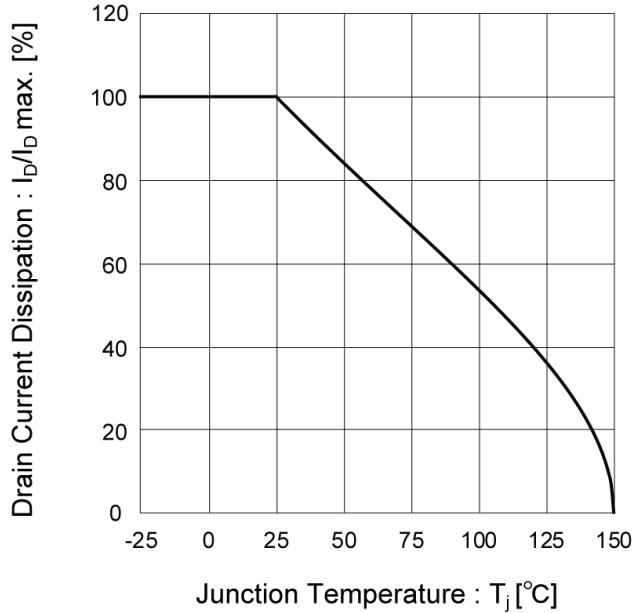
**Fig.9 Gate Threshold Voltage vs.  
Junction Temperature**



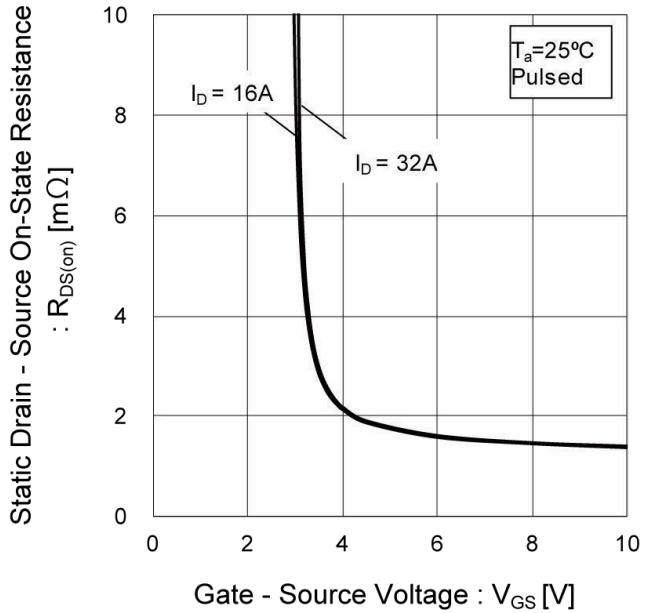
**Fig.10 Forward Transfer Admittance vs.  
Drain Current**



**Fig.11 Drain Current Derating Curve**

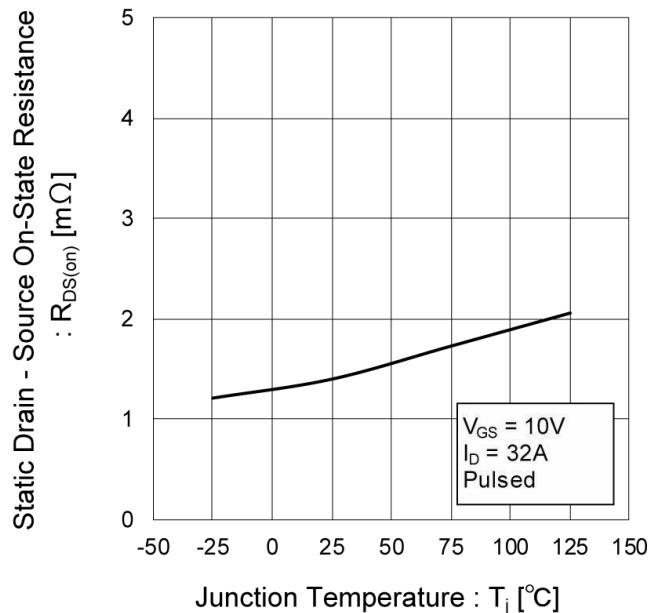


**Fig.12 Static Drain - Source On - State  
Resistance vs. Gate Source Voltage**

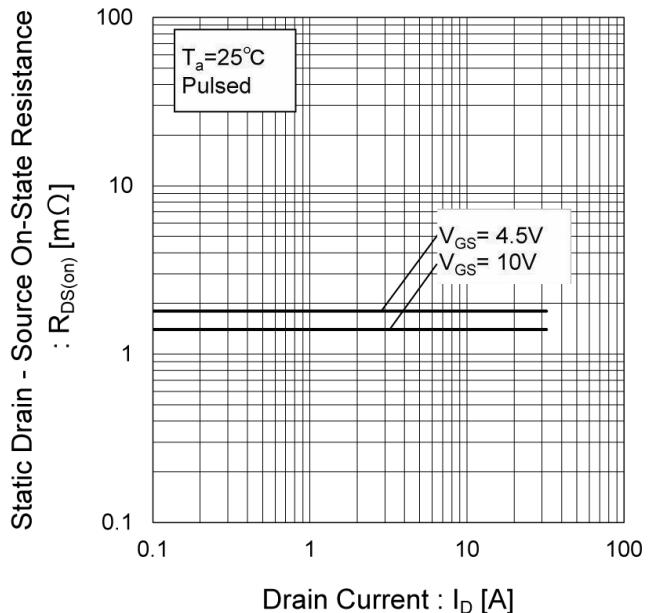


### Electrical characteristic curves

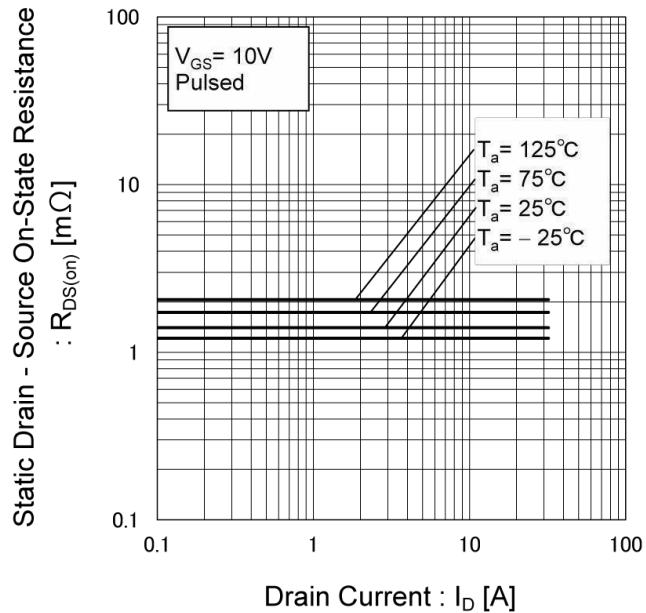
**Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature**



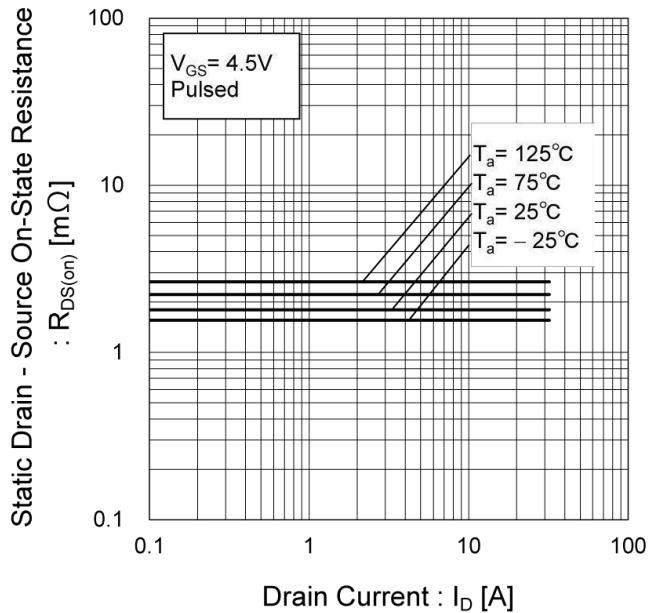
**Fig.14 Static Drain - Source On - State Resistance vs. Drain Current ( $I_D$ )**



**Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)**

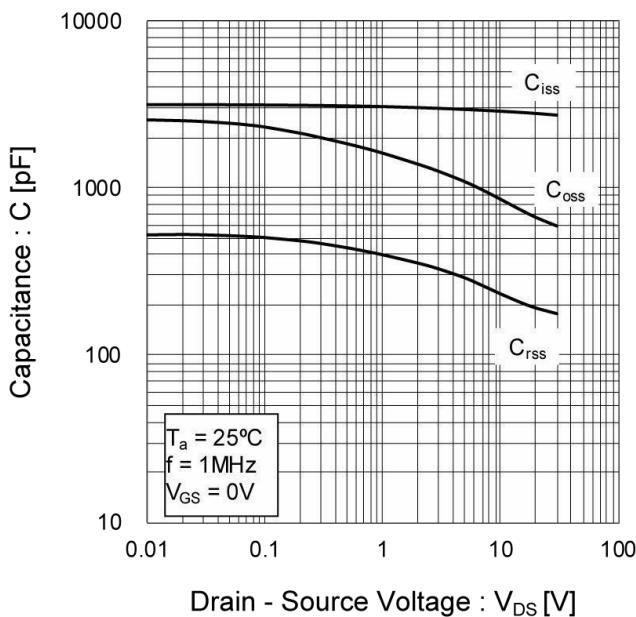


**Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)**

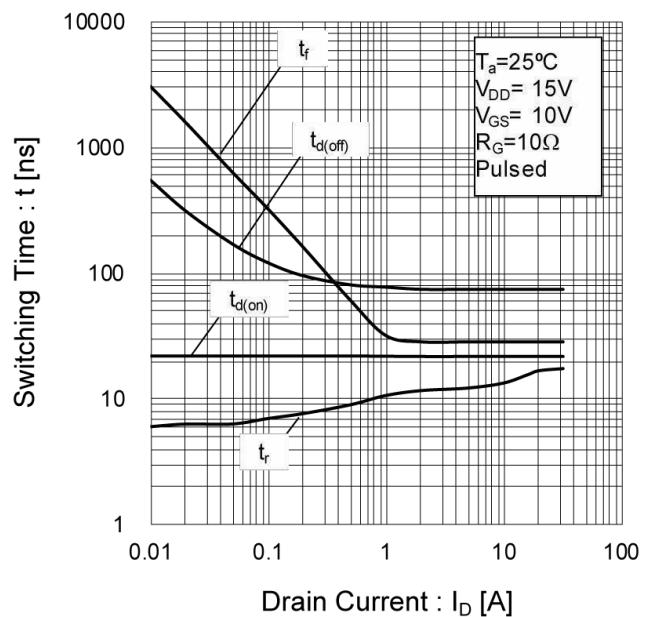


## Electrical characteristic curves

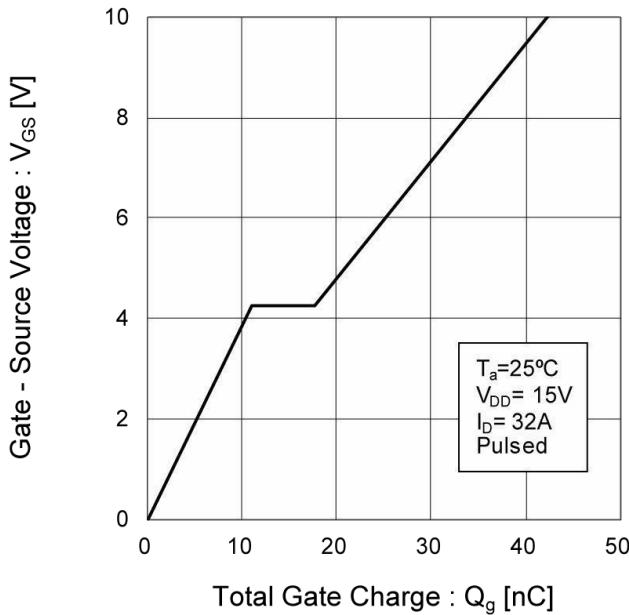
**Fig.17 Typical Capacitance vs.  
Drain - Source Voltage**



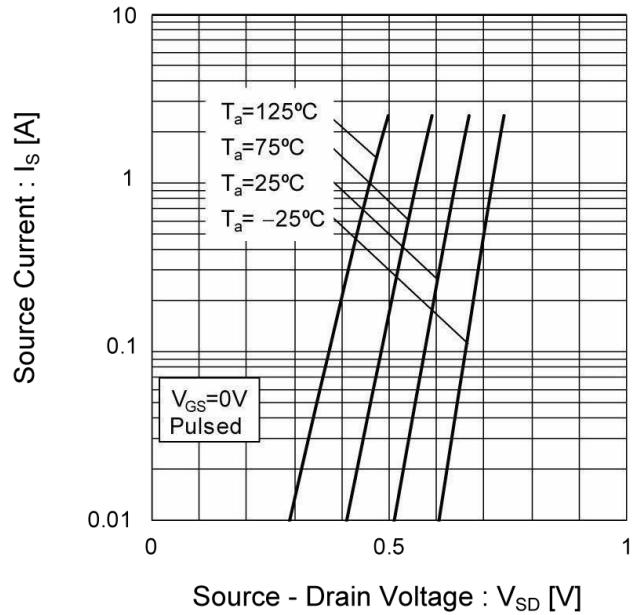
**Fig.18 Switching Characteristics**



**Fig.19 Dynamic Input Characteristics**

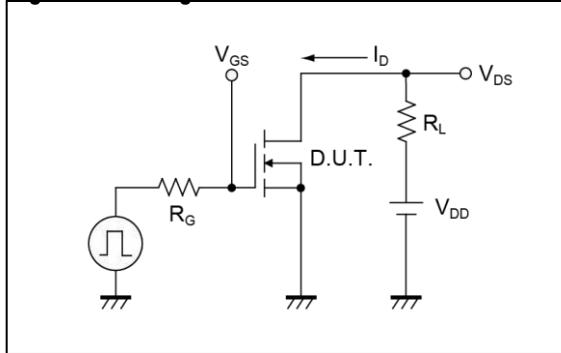


**Fig.20 Source Current vs.  
Source Drain Voltage**

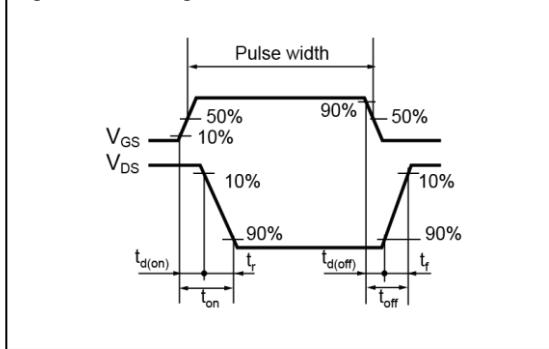


## Measurement circuits

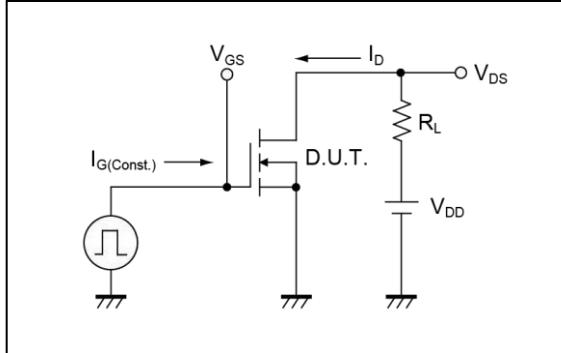
**Fig.1-1 Switching Time Measurement Circuit**



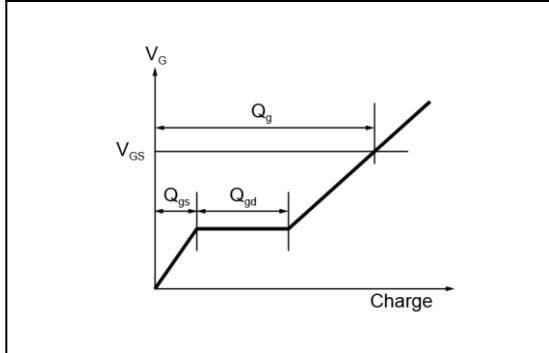
**Fig.1-2 Switching Waveforms**



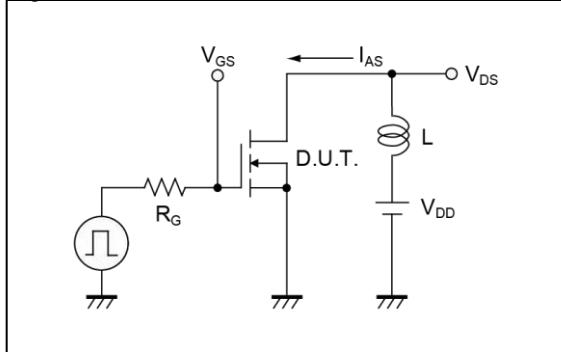
**Fig.2-1 Gate Charge Measurement Circuit**



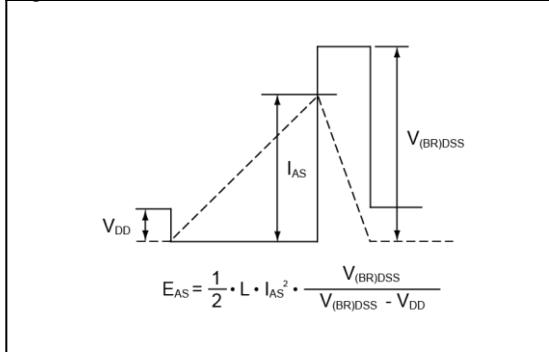
**Fig.2-2 Gate Charge Waveform**



**Fig.3-1 Avalanche Measurement Circuit**



**Fig.3-2 Avalanche Waveform**

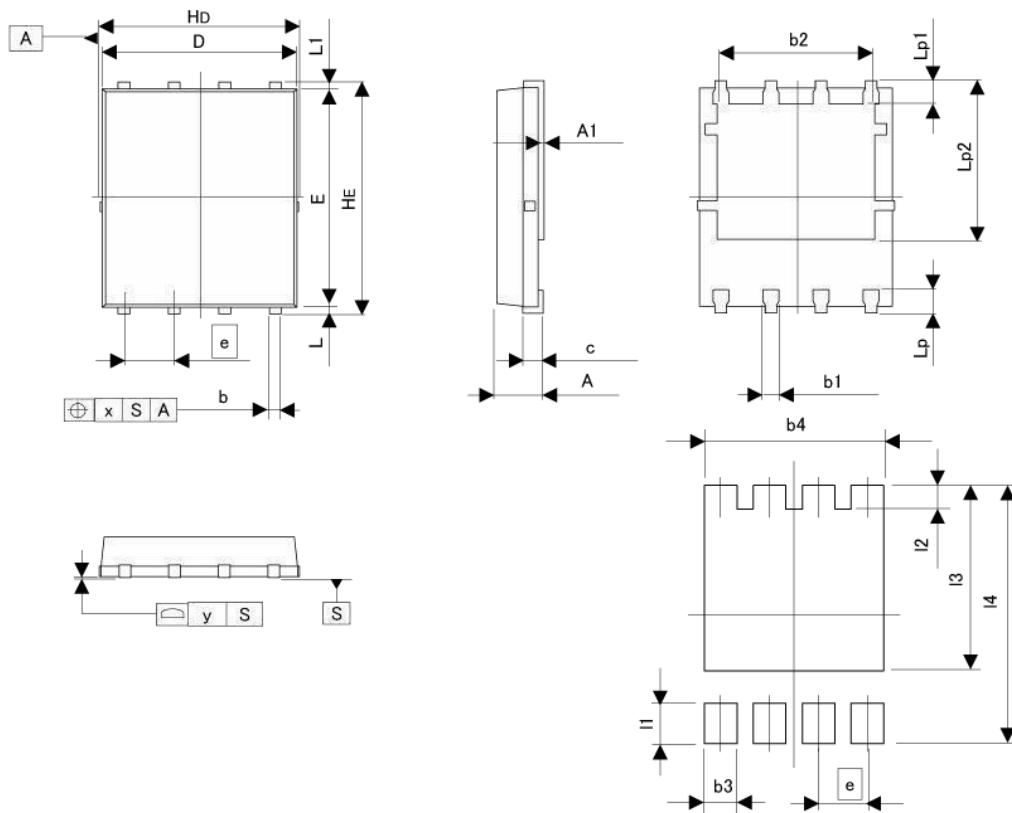


## Notice

This product might cause chip aging and breakdown under the large electrified environment.  
Please consider to design ESD protection circuit.

**Dimensions**

HSOP8 ( 5 x 6 )



Pattern of terminal position areas  
[Not a pattern of soldering pads]

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.90	1.10	0.035	0.043
A1	0.00	0.05	0.000	0.002
b	0.24	0.42	0.009	0.017
b1	0.29	0.49	0.011	0.019
b2	3.81	4.21	0.150	0.166
c	0.20	0.30	0.008	0.012
D	4.80	5.00	0.189	0.197
E	5.60	5.80	0.220	0.228
e	1.27		0.050	
HD	4.90	5.10	0.193	0.201
HE	5.90	6.10	0.232	0.240
L	0.07	0.25	0.003	0.010
L1	0.07	0.25	0.003	0.010
Lp	0.50	0.70	0.020	0.028
Lp1	0.52	0.72	0.020	0.028
Lp2	3.92	4.32	0.154	0.170
x	-	0.10	-	0.004
y	-	0.10	-	0.004

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b3	-	0.59	-	0.023
b4	-	4.21	-	0.166
I1	-	0.80	-	0.031
I2	-	0.82	-	0.032
I3	-	4.32	-	0.170
I4	-	6.10	-	0.240

Dimension in mm/inches